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AMENDMENTS TO THE SPECIFICATION

Please revise paragraph [0019] as follows.

[0019] FIG. 3 is a diagram showing a data driver according to the first embodiment of the invention. The data driver includes a shift register 1, a sample and hold register 2, three digital-to-analog converters 3a, 3b and 3c, a gamma multiplexer 4, three unit gain buffers 5a, 5b and 5c, and a data bus 6. The shift register 1 receives image data of the three primary colors in serial through a serial data bus 15 and outputs the image data of the three primary colors in parallel within each of scan durations of the horizontal lines. The sample and hold register 2 is turned on by the shift register 1 and acquires the image data from the shift register 1 through the data bus 6. The gamma multiplexer 4 outputs gamma reference voltages for the three primary colors in a predetermined sequence of the primary colors within one scan duration according to a selection signal from the line 11. The gamma reference voltages for the three primary colors are output during corresponding time slots R:ON, G:ON and B:ON. The digital-to-analog converters 3a, 3b and 3c receive the image data of and the gamma reference voltages for the three primary colors respectively from the sample and hold register 2 and the gamma multiplexer 4, and output calibrated image signals of the three primary colors, respectively. The unit gain buffers 5a, 5b and 5c respectively receive the calibrated image signals of the three primary colors from the digital-to-analog converters 3a, 3b and 3c through the switches Rsw, Gsw and Bsw in the predetermined sequence of the primary colors. The switches Rsw, Gsw and Bsw are closed during the corresponding time slots R:ON, G:ON and B:ON, whereby the sub-pixel data is output to [[the]] a pixel array through the R, G and B data lines.